

REGISTRATION FORM

ANNA UNIVERSITY REGIONAL CENTRE, COIMBATORE

DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING

Two Day Hands-on Training Program on

IC System Design using Mentor Graphics
EDA Tool
22nd & 23rd MARCH 2013

Name:
Organisation:
Institution:
Address for
Communication:

Mobile No:
E-mail ID:

CATEGORY:

Student
Academics
Industrialists

Signature of the Participant

Signature of the Principal or HoD with Seal

ORGANIZING COMMITTEE

Chairman:

Dr. Rajamaniccam L. Sai
Regional Director
Anna University
Regional Centre, Coimbatore
Coimbatore – 641 047

Coordinator:

Dr. V.R. Vijaykumar
Associate Professor
Department of Electronics and Communication Engg.,
Regional Centre, Coimbatore
Coimbatore – 641 047

Co-Coordiators:

1. Mr. S. Saravanakumar, AP/ECE
2. Mr. K. Thiruvankadam, TF/ECE
3. Mr. O. Vignesh, TF/ECE
4. Mr. S. Saravanan, TF/ECE

Anna University, Regional Centre, Coimbatore

CONTACT ADDRESS

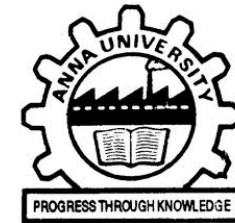
For any queries and to send completed registration forms:

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Two Day Hands-on
Training Programme on

IC SYSTEM DESIGN USING MENTOR GRAPHICS EDA TOOL

22nd & 23rd MARCH 2013



ORGANISED BY

IEEE STUDENT BRANCH

DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING

ANNA UNIVERSITY
REGIONAL CENTRE, COIMBATORE

Mettupalayam Road, Jothipuram
Coimbatore – 641 047

ABOUT THE UNIVERSITY

Anna University was established on 4th September 1978 as a unitary type of University. It offers higher education in Engineering, Technology and allied Sciences relevant to the current and projected needs of the society. Besides promoting research and disseminating knowledge gained there from, it fosters cooperation between the academic and industrial communities. Anna University is of the affiliated type and is a member of the Association of Indian Universities, the Association of Commonwealth Universities and Partner of UNESCO International Center for Engineering Education (UICEE). With proven capabilities both in academic and research areas, Anna University was able to impart quality and excellence in Technical Education. Anna University has four regional centres at Coimbatore, Madurai, Tiruchirappalli and Tirunelveli to sphere head the academic activities throughout the state.

ABOUT THE DEPARTMENT

The Anna University, Regional Center, Coimbatore is functioning at Mettupalayam Road, Jothipuram, Coimbatore. It has nine PG departments and it offers twenty one PG Programmes. The Department of Electronics and Communication Engineering was established in the year 2007. The Department offers M.E degree courses in Applied Electronics, Communication Systems, Embedded System Technologies and VLSI Design both full time and part time mode. The Department is equipped with excellent infrastructure and state-of-the-art equipment to explore the technological challenges in the areas of Electronics, Communication Networks, VLSI Design, Signal Processing, ASIC Design, RF Design and Digital System Design.

ABOUT MENTOR GRAPHICS

Mentor Graphics® provides software and hardware design solutions enabling companies to develop faster and efficient electronic products more cost-effectively. It helps engineers in overcoming the design challenges in the increasingly complex worlds of board and SoC designs. Mentor Graphics offers the most comprehensive IC design, verification, DFM and test technologies. Tight collaboration with foundries, IC design houses, systems companies, research and development labs, and industry standards organizations ensures Mentor Graphics tools meet state-of-the-art requirements and deliver real competitive value.

SCOPE OF THE TRAINING

This training program has emphasis to provide a platform for interaction and to discuss about the recent advances, new paradigms, technological trends and challenges in the field of IC design and fabrication. Analog/Mixed-Signal verification tools offer a language-neutral verification environment for complex Analog/Mixed-Signal System-on-Chip designs. Also this training provides an excellent opportunity to exchange ideas on the topics of importance along with thought provoking technical sessions like IC designs and EDA tools, by eminent experts from Mentor Graphics and University Departments.

WHO SHOULD ATTEND?

The faculties, final year UG and PG students from Engineering Colleges who have interest in VLSI layout design and doing research in the area of VLSI Design using Back End tools.

RESOURCE PERSONS

Experts from University Departments and Industry.

TOPICS TO BE COVERED

- **ASIC Full custom and Semicustom IC Design flow**
- **Concept of Physical Design & Verification**
- **Analog and Mixed Signal Flow**
- **Over view of Parasitic Extraction**
- **Basic & Advanced Circuit Design using Mentor Graphics**
- **Combinational and Sequential Circuits Simulation using Mentor Graphics**

REGISTRATION

Registration for the training program can be done using the attached form or photo copy of the form. The completed form could be sent by post to the Coordinator. **The Registration is restricted to first 40 participants only. For spot registration contact the coordinator.**

FEE DETAILS

	IEEE Members	Non-IEEE
Students / Faculty	: ₹ 600/-	₹ 750/-
Industry delegates	: ₹ 800/-	₹ 1000/-

DD should be drawn in favour of

“IEEE Student Branch – AUCBE”

Payable at Coimbatore, Tamil Nadu, India

IMPORTANT DATE

Last Date of Registration: **18th March 2013**